

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

1. (currently amended) A logic drawing entry apparatus for processing a plurality of drawing sheets for computer aided design of logic circuits, each of said plurality of drawing sheets indicating a logic circuit having at least one symbol, comprising:

a means for creating an inter-drawing diagram file which describes respective positions of said plurality of drawing sheets on one screen and attributes of said ~~interrelation in a plurality of drawing sheets; and~~

inter-drawing indication means for indicating, on ~~one said~~ screen, ~~a said plurality of drawing sheets according to the description in said inter-drawing diagram file by the miniaturized~~ miniaturizing the size of each of said plurality of drawing sheets, said miniaturized drawing sheets are connected by nets, according to the description in the inter-drawing diagram file which has been created and

net drawing means for drawing nets among said plurality of drawing sheets miniaturized and indicated on said screen, said nets indicating connection relations among a plurality of symbols contained in different ones of said plurality of drawing sheets.

2. (currently amended) The logic drawing entry apparatus of claim 1, further comprising:

inter-drawing diagram editing means for implementing editing works on each of ~~a said plurality of said drawing sheets when the said plurality of said drawings~~ drawing sheets are indicated on one said screen.

3. (currently amended) The logic drawing entry apparatus of claim 2, wherein said inter-drawing diagram editing means, further, modifies the position of each drawing sheet on ~~an indication~~ said screen.

4. (currently amended) The logic drawing entry apparatus of claim 2, wherein said inter-drawing diagram editing means, further, modifies the attributes of each drawing sheet on ~~an indication~~ said screen.

5. (currently amended) The logic drawing entry apparatus of claim 1, further comprising:

inter-drawing connection counting means for counting the number of connections ~~between a plurality of said drawing sheets about symbols included in a plurality of said~~ any combinations of said two of said plurality of drawing sheets; and

a net connection relation drawing means for drawing net connection relations between said drawings sheets based on the number of inter-drawing connections counted by said inter-drawing connection counting means.

6. (original) The logic drawing entry apparatus of claim 5, wherein said net connection relation drawing means has a function of modifying the indications of the nets according to said number of inter-drawing connections.

7. (previously presented) The logic drawing entry apparatus of claim 1, further comprising:

drawing name modifying means for selecting a plurality of said drawings sheets and modifying the name of said plurality of drawing sheets, in ascending or descending order.

8. (previously presented) The logic drawing entry apparatus of claim 7, wherein said drawing name modifying means , further, designates intervals between the names of said plurality of drawing sheets.

9. (currently amended) A logic drawing entry apparatus for processing ~~of drawing sheets in which are indicated a plurality of symbols, and nets expressing connection relations between the symbols, the~~ a drawing sheet indicating a logic circuit which has a plurality of symbols and nets connecting among said symbols, logic drawing entry apparatus comprising:

symbol selecting means for selecting ~~symbols~~ a symbol to be moved and ~~positions~~ a position to which the selected ~~symbols are to be moved~~ symbol moves;

judging means for judging whether or not a symbol exists at said position selected;

symbol moving means for moving said selected symbols to said ~~positions~~ position if there is no symbol at said selected position;

symbol swapping means for swapping ~~positions of said selected symbols with the positions to which said selected symbols are to be moved when symbols, other than said selected symbols, exist at the positions to which said selected symbols are to be moved~~ said

selected symbol for a symbol at said selected position if there is a symbol at said selected position, so that positions of said selected symbol and said symbol at said selected position asre swapped each other; and

net redrawing means for redrawing nets for said selected symbols after the movement or swap while keeping the connection relations between said selected symbols before the movement.

10. (canceled)

11. (currently amended) The logic drawing entry apparatus of claim 9, further comprising arranging means for arranging a plurality of selected symbols on a said drawing sheet ~~in to form~~ a column or a row.

12. (previously presented) The logic drawing entry apparatus of claim 11, wherein said arranging means, further, designates intervals between symbols.

13. (canceled)

14. (currently amended) A logic drawing entry apparatus for ~~processing of drawings in which hierarchic symbols, which is an expression of the logic circuit at a certain hierarchical level as the symbol, having a plurality of pins are described, the logic drawing entry apparatus~~ drawing a plurality of drawing sheets for hierarchical design of a logic circuit, comprising:

~~a hierarchic symbol drawing means for drawing by dividing said hierarchic symbols;~~ judging means to determine if a particular level of hierarchical design of a logic circuit comprises a plurality of symbols, each of which belongs to the same level as said particular level;

drawing means for drawing a plurality of drawing sheets by dividing said plurality of symbols into individual symbols so that each of said drawing sheets comprises at least one of said symbols, if said judging means determines that said particular level of hierarchical design of a logic circuit comprises a plurality of symbols, each of which belongs to the same level as said particular level; and

a net drawing means for drawing nets for individual symbols which have been drawn each of said symbols.

15. (previously presented) The logic drawing entry apparatus of claim 1, wherein said plurality of miniaturized drawing sheets are drawn in a shape of a block diagram.

16. (new) A method of displaying a relationship between a plurality of drawing sheets for computer aided design of logic circuits, the method comprising:

displaying the plurality of drawing sheets according to the description in a inter-drawing diagram file, each drawing sheet of the plurality of drawing sheets reduced in size to allow the displayed plurality of drawing sheets to exist on a single screen; and

drawing nets among the plurality of reduced drawing sheets, the nets indicating the relative relationship among a plurality of symbols contained in each drawing sheet of the plurality of drawing sheets.